

Refine Search

Search Results -

Terms	Documents
L6 AND L10	149

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
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 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L16

Refine Search

Recall Text 

Clear

Interrupt

Search History

DATE: Friday, July 15, 2005 [Printable Copy](#) [Create Case](#)

<u>Set Name</u> side by side	<u>Query</u>	<u>Hit Count</u>	<u>Set Name result set</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI; PLUR=YES; OP=OR</i>			
<u>L16</u>	L6 AND L10	149	<u>L16</u>
<u>L15</u>	L14 AND l10	29	<u>L15</u>
<u>L14</u>	L13 AND 250/\$.CCLS.	2466	<u>L14</u>
<u>L13</u>	(CMOS)	124274	<u>L13</u>
<u>L12</u>	L11 and L5	1	<u>L12</u>
<u>L11</u>	L10 and L6	149	<u>L11</u>
<u>L10</u>	(pixel) same (readout) near10 (multiple or many or lot adj times)	254	<u>L10</u>
<u>L9</u>	(pixel) same (readout adj multiple adj times)	0	<u>L9</u>
<u>L8</u>	(pixel adj readout adj multiple adj times)	0	<u>L8</u>
<u>L7</u>	L6 AND L5	1	<u>L7</u>

<u>L6</u>	(collect\$3 or transfer or readout adj capacitor) and (CMOS OR CCD)	101147	<u>L6</u>
<u>L5</u>	(pixel adj element\$1) near10 (substrate) near10 (insulat\$3 adj layer)	5	<u>L5</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L4</u>	(pixel adj element\$1) near5 (substrate) near6 (insulating adj layer)	2	<u>L4</u>
<i>DB=USPT; PLUR=YES; OP=OR</i>			
<u>L3</u>	5471515.pn.	1	<u>L3</u>
<u>L2</u>	6021172.pn.	1	<u>L2</u>
<u>L1</u>	6180937.pn.	1	<u>L1</u>

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L11 and L5	1

Database:

US Pre-Grant Publication Full-Text Database
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 IBM Technical Disclosure Bulletins

Search:

L12

Search History

DATE: Friday, July 15, 2005
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<u>Set Name</u> side by side	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u> result set
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI; PLUR=YES; OP=OR</i>			
<u>L12</u>	L11 and L5	1	<u>L12</u>
<u>L11</u>	L10 and L6	149	<u>L11</u>
<u>L10</u>	(pixel) same (readout) near10 (multiple or many or lot adj times)	254	<u>L10</u>
<u>L9</u>	(pixel) same (readout adj multiple adj times)	0	<u>L9</u>
<u>L8</u>	(pixel adj readout adj multiple adj times)	0	<u>L8</u>
<u>L7</u>	L6 AND L5	1	<u>L7</u>
<u>L6</u>	(collect\$3 or transfer or readout adj capacitor) and (CMOS OR CCD)	101147	<u>L6</u>
<u>L5</u>	(pixel adj element\$1) near10 (substrate) near10 (insulat\$3 adj layer)	5	<u>L5</u>